(A (ESTD-1995) DEPARTMENT OF ELECTR

(ESTD-1995) DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

<u>CO-PO Mapping of Project in the area of Application of Energy</u> <u>Management System</u>

<u>Title of the Project</u>: A Hybrid Cascaded Multilevel Converter for Battery Energy Management applied in Electric Vehicles.

Area of the Project: Power Semiconductor drives

Methodology: Simulation

Name of the Supervisor: Dr. V. NAGA BHASKAR REDDY M.Tech, Ph. D, SMIEEE, MISTE

 Name of the Students:
 A. V. SANDEEP (18095A0241)

 G. MEGHANA (17091A0233)
 P. PAVANI (18095A0224).

Abstract:

In electric vehicle (EV) energy storage systems, a large number of battery cells are usually connected in series to enhance the output voltage for motor driving. The difference in electrochemical characters will cause state-ofcharge (SOC) and terminal voltage imbalance between different cells. In this paper, a hybrid cascaded multilevel converter which involves both battery energy management and motor drives is proposed for EV. In the proposed topology, each battery cell can be controlled to be connected into the circuit or to be bypassed by a half-bridge converter. All half bridges are cascaded to output a staircase shape dc voltage. Then, an H-bridge converter is used to change the direction of the dc bus voltages to make up ac voltages. The outputs of the converter are multilevel voltages with less harmonics and lower dv/dt, which is helpful to improve the performance of the motor drives. By separate control according to the SOC of each cell, the energy utilization ratio of the batteries can be improved. The imbalance of terminal voltage and SOC can also be avoided, fault tolerant can be easily realized by modular cascaded circuit, so the life of the battery stack will be extended. Simulations are implemented to verify the performance of the proposed converter.



Head of Department Electrical & Electronics Engineering RGM College of Engineering & Tech. Nandyal-518 501,Kurnool(Dist) A.P

Dr. T. JAYACHANDRA PRASAD M.E.Ph.D., FIE, FIETE, MNAFEN, MISTE, MIEEE PRINCIPAL R G M College of Engg. & Tech., (Autonomous) e NANDYAL-518 501, Kurnool (Dt), A.P.



(AUTONOMOUS)

(ESTD-1995) DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Process of CO-PO attainment for Project thesis of IV-year Main Project

Course Outcomes:-

- 1. To identify the problem formulation of the project after literature survey or study of existing technology
- 2. To analyze the basic concepts of the project in correlation with the engineering knowledge
- 3. To apply the concepts of technology with modern tool usage to overcome the problem.
- 4. To formulate the solution and to design simulation and prototype of the solution with the engineering knowledge.

CO-PO Mapping:-

CO/P	PO	PO1	PO1	PO1								
0	1	2	3	4	5	6	7	8	9	0	1	2
CO1	2	-	-	-	-	-	-	-	3	-	-	-
CO2	2	-	2	-	-	-	-	-	3	-	-	-
CO3	2	-	-	-	-	-	-	-	3	-	3	-
CO4	2	_	2	-	2	-	-	-	3	-	3	-

Evaluation:-

Project	$ 100 = - \cdot$	External evaluation	This end viva voce in project work for 100 marks
work	50	Internal evaluation	These 50 marks will be based on the performance of the student in the project reviews apart from attendance and regularity



Head of Department Electrical & Electronics Engineering RGM College of Engineering & Tech... Nandyal-518 501,Kurnool(Dist) A.P

Dr. T. JAYACHANDRA PRASAD M.E,Ph.D.,FIE,FIETE,MNAFEN,MISTE,MIEEE PRINCIPAL R G M College of Engg. & Tech., (Autonembus) e NANDYAL-518 501, Kurnool (Dt), A.P.

RAJEEV GANDHI MEMORIAL COLLEGE OF ENGINEERING & TECHNOLOGY

(AUTONOMOUS)

(ESTD-1995) DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Table: Percentage Weightages for each CO

s	.NO	REG	IM 50M	EM grade	TM 150M	EM	%IM	%EM	CO1	CO2	CO3	CO4	N.CO1	N.CO2	N.CO3	N.CO4
	1	18095a0241	49	10	143	94	98	94.00	25.57	31.57	19.05	19.04	95.91	94.71	95.24	95.24
	2	17091a0233	45	10	143	98	90	98.00	25.04	32.10	19.05	19.04	93.91	96.31	95.24	95.24
	3	18095a0224	43	9	128	85	86	85.00	22.78	28.37	17.05	17.04	85.42	85.12	85.25	85.25

Table: Weightage marks for each CO

	CO1	CO2	CO3	CO4
INTERNAL	40	20	20	20
EXTERNAL	20	40	20	20
AVERAGE	26.66	33.33	19.99	19.99

Table: Percentage Attainment Values for each CO

		Co1			C02		C03		Co4	
Above & Equal 60%	3	3		3	3	3	3	3	3	
Between 40-60%		2		0	2	0	2	0	2	
Below40%		1		0	1	0	1	0	1	
Total students	3			3		3		3		
Attainment value		3.00			3.00		3.00		3.00	
% of attainment		100.00			100.00		100.00		100.00	
Attained or not(GREATER 50% Y,NOT MEANS N		Y			Y		Y		Y	

Head of Department Electrical & Electronics Engineering RGM College of Engineering & Tech. Nandyal-518 301,Kurnool(Dist) & P

Dr. T. JAYACHANDRA PRASAD M.E,Ph.D.,FIE,FIETE,MNAFEN,MISTE,MIEEE PRINCIPAL R G M College of Engg. & Tech., (Autonomolis) e NANDYAL-518 501, Kurnool (Dt), A.P.

A Hybrid Cascaded Multilevel Converter for Battery Energy Management Applied in Electric Vehicles

A Main project report submitted in partial fulfilment of the requirements for the award of the degree of

BACHELOR OF TECHNOLOGY

IN

ELECTRICAL AND ELECTRONICS ENGINEERING

Submitted by A. V. SANDEEP (18095A0241) G. MEGHANA (17091A0233) P. PAVANI (18095A0224)

Under the Esteemed Guidance of Dr. V. NAGA BHASKAR REDDY M.Tech, Ph. D, SMIEEE, MISTE Professor & H.O.D in Dept. of E.E.E



(ESTD-1995)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

RAJEEV GANDHI MEMORIAL COLLEGE OF ENGINEERING &

TECHNOLOGY

(AUTONOMOUS)

(Affiliated to JNTU- Anantapuram, Approved by AICTE- New Delhi,

Accredited by NBA-New Delhi, Accredited by NAAC of UGC with

'A+' Grade) Nandyal-518501, Kurnool Dist., A.P



(2017 - 2021)

Head of Department

Electrical & Electronics Engineering RGM College of Engineering & Tech.

Nandyal-518 501, Kurnool/Dist) A.P.

Di R(

Dr. T. JAYACHANDRA PRASAD M.E.Ph.D., FIE.FIETE, MNAFEN, MISTE, MIEEE PRINCIPAL R G M College of Engg. & Tech., (Autonomous) NANDYAL-518 501, Kurnool (Dt), A.P.

RAJEEV GANDHI MEMORIAL COLLEGE OF ENGINEERING &

TECHNOLOGY

(AUTONOMOUS)

(Affiliated to JNTU- Anantapuram, Approved by AICTE- New Delhi, Accredited by NBA-New Delhi, Accredited by NAAC of UGC with 'A+' Grade) Nandyal-518501, Kurnool Dist., A.P



(ESTD-1995)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

BONAFIDE CERTIFICATE

This is to certify that the thesis entitled "A HYBRID CASCADED MULTILEVEL CONVERTER FOR BATTERY ENERGY MANAGEMENT IN ELECTRIC VEHICLES" that is being submitted by A. V. SANDEEP (18095A0241), G. MEGHANA (17091A0233), P. PAVANI (18095A0224) have carried out the main project for the fulfilment of the award of Bachelor or Technology in Electrical and Electronics Engineering in Rajeev Goodhi Memorial College of Engineering & Technology (Autonomous) and this is a record of the work done by them during the year 2020 - 21.

Project Guide lend of the Department Dr. V. NAGA BHASKAR-REDDY A BHASKAR REDDY Professo rofessor FEEL, RGMCET IANDRA PRASAD VAFEN MISTE MIEEE Electrical & Electronics **RGM Collean of Ep** anga inger. & Tech., Nandyal-518 501, Kum NANDYAL-518 501, Kurnool (Dt), A.P. miner Date:

ABSTRACT

In electric vehicle (EV) energy storage systems, a large number of battery cells are usually connected in series to enhance the output voltage for motor driving. The difference in electrochemical characters will cause state-of-charge (SOC) and terminal voltage imbalance between different cells. In this paper, a hybrid cascaded multilevel converter which involves both battery energy management and motor drives is proposed for EV. In the proposed topology, each battery cell can be controlled to be connected into the circuit or to be bypassed by a half-bridge converter. All half bridges are cascaded to output a staircase shape dc voltage. Then, an H-bridge converter is used to change the direction of the dc bus voltages to make up ac voltages. The outputs of the converter are multilevel voltages with less harmonics and lower dv/dt, which is helpful to improve the performance of the motor drives. By separate control according to the SOC of each cell, the energy utilization ratio of the batteries can be improved. The imbalance of terminal voltage and SOC can also be avoided, fault-tolerant can be easily realized by modular cascaded circuit, so the life of the battery stack will be extended. Simulations are implemented to verify the performance of the proposed converter.



Head of Department Electrical & Electronics Engineering RGM College of Engineering & Tech. Nandyal-518 501,Kurnool(Dist) & P

Dr. T. JAYACHANDRA PRASAD M.E.Ph.D., FIE, FIETE, MNAFEN, MISTE, MIEEE RINCIPAL llege of Engg. & Tech., utonomous NANDYAL-518 501, Kurnool (Dt), A.P.